

Attorney Dkt. No. 5732-00100

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:
Chhor et al.

Filed: Herewith

Serial No. 09/928,767

For: MOLDED MEMORY MODULE AND
METHOD OF MAKING THE MODULE
ABSENT A SUBSTRATE SUPPORT

Attorney Dkt. No. 5732-00100

I hereby certify that this correspondence (consisting of 1 page) is
being transmitted via facsimile to USPTO, TC2800, Before Final,
703/872-9318 on the date indicated below:

3/5/02

Date

Pamela Gerik

Pamela Gerik

POWER OF ATTORNEY BY ASSIGNEE

Under the provisions of 37 C.F.R. § 3.71, the undersigned assignee of record of the entire interest
in the above-identified patent/patent application by virtue of an assignment recorded (check as
applicable):



Concurrently herewith



Date Recorded



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elects to conduct the prosecution of the application/maintenance of the patent to the exclusion of the
inventor(s). The undersigned hereby declares that he has reviewed the above-referenced assignment and
hereby declares that, to the best of his knowledge, title is in the Assignee, and further declares that all
statements made herein of his own knowledge are true and that all statements made on information and
belief are believed to be true. The assignee hereby revokes any previous powers of attorney and appoints
the following to prosecute this application/maintain this patent and transact all business in the Patent
and Trademark Office connected therewith:

Liza K. Toth, Reg. No. 31,065 of Matrix Semiconductor, Inc.
Kevin L. Daffer, Reg. No. 34,146; B. Noel Kivlin, Reg. No. 33,929; Eric B. Meyertons, Reg.
No. 34,876; and Gentry E. Crook, Reg. No. 44,633 of Conley, Rose & Tayon LLP

Please direct all communications to: Conley, Rose & Tayon, P. O. Box 398, Austin, Texas 78767,
Tel. No.: (512) 476-1400, to the attention of: Kevin L. Daffer

ASSIGNEE
MATRIX SEMICONDUCTOR, INC.

Date:

2/21/02

BY:

Liza K. Toth

Liza K. Toth, VP, Intellectual Property